

[Skip to Main Content](#)

[IEEE.org](#) | [IEEE Xplore Digital Library](#) | [IEEE Standards Association](#) | [Spectrum Online](#) | [More IEEE Sites](#)



Search Term(s)

[Advanced Search](#) | [Preferences](#) | [Search Tips](#)








**[Browse](#)**

- [Journals & Magazines](#)
- [Conference Proceedings](#)
- [Standards](#)
- [Books](#)
- [Educational Courses](#)
- [Technology Surveys](#)
  
- [My Settings](#)
  - [Alerts](#)
  - [Purchase History](#)
  - [Saved Searches](#)
  - [Sign In](#)
  - [What can I access?](#)
- [Cart](#)

[Terms of Use](#) | [Feedback](#) |  [Help](#)

**Search Results**

You searched for: **multiplication with DSP and table**

-  [System Search](#)
-  [Alerts](#)
-  [Purchase History](#)
-  [Saved Searches](#)
-  [Sign In](#)
-  [What can I access?](#)
-  [Cart](#)

Results per Page

Showing 1 - 25 of 34 results

[Next»](#)

Sort By:

[Select All on Page](#) | [Deselect All](#)



### A quadratic residue processor for complex DSP applications

Bayoumi, M.;

Acoustics, Speech, and Signal Processing. IEEE International Conference on ICASSP '87.

Volume: 12

Digital Object Identifier: 10.1109/ICASSP.1987.1169675

Publication Year: 1987 , Page(s): 475 - 478

IEEE Conferences



[AbstractPlus](#) | Full Text: [PDF](#) (192 KB)



[Quick &](#)  
[Abstract](#)



### Medium-Grain Cells for Reconfigurable DSP Hardware

Myjak, M.J.; Delgado-Frias, J.G.;

Circuits and Systems I: Regular Papers, IEEE Transactions on

Volume: 54 , Issue: 6

Digital Object Identifier: 10.1109/TCSI.2007.895384

Publication Year: 2007 , Page(s): 1255 - 1265

IEEE Journals



[AbstractPlus](#) | Full Text: [PDF](#) (472 KB)



[Quick &](#)  
[Abstract](#)



### A simple and ultra-fast DSP-based space vector PWM algorithm and its implementation on a two-level inverter covering undermodulation and overmodulation

Filho, N.P.; Pinto, J.O.P.; Borges da Silva, L.E.; Bose, B.K.;

Industrial Electronics Society, 2004. IECON 2004. 30th Annual Conference of IEEE

Volume: 2

Digital Object Identifier: 10.1109/IECON.2004.1431750

Publication Year: 2004 , Page(s): 1224 - 1229 Vol. 2

IEEE Conferences



[AbstractPlus](#) | Full Text: [PDF](#) (1999 KB)



[Quick &](#)  
[Abstract](#)



### Implementation of a fixed-point MPEG-4 scalable lossless coding encoder

Choong Sang Cho; Yong-Hwan Kim; Hwa Seon Shin; Byeong Ho Choi;  
Broadband Multimedia Systems and Broadcasting (BMSB), 2010 IEEE International Symposium on  
 Digital Object Identifier: [10.1109/ISBMSB.2010.5463167](https://doi.org/10.1109/ISBMSB.2010.5463167)  
 Publication Year: 2010 , Page(s): 1 - 5

IEEE Conferences

 [AbstractPlus](#) | Full Text: [PDF](#) (265 KB)



## An Optimized Design of Bit Plane Prediction Based Image Compression Algorithm on DSP

Zheng Zhao; Jiang Hongxu; Li Bo;  
Information Science and Engineering (ICISE), 2009 1st International Conference on  
 Digital Object Identifier: [10.1109/ICISE.2009.291](https://doi.org/10.1109/ICISE.2009.291)  
 Publication Year: 2009 , Page(s): 1157 - 1160

IEEE Conferences

 [AbstractPlus](#) | Full Text: [PDF](#) (246 KB)



## Fast combinatorial RNS processors for DSP applications

Di Claudio, E.D.; Piazza, F.; Orlandi, G.;  
Computers, IEEE Transactions on  
 Volume: 44 , Issue: 5  
 Digital Object Identifier: [10.1109/12.381948](https://doi.org/10.1109/12.381948)  
 Publication Year: 1995 , Page(s): 624 - 633

IEEE Journals

 [AbstractPlus](#) | Full Text: [PDF](#) (848 KB)



## A universal nonlinear component and its application to WSI

Jain, V.K.; Wadekar, S.A.; Lei Lin;  
Components, Hybrids, and Manufacturing Technology, IEEE Transactions on  
 Volume: 16 , Issue: 7  
 Digital Object Identifier: [10.1109/33.257869](https://doi.org/10.1109/33.257869)  
 Publication Year: 1993 , Page(s): 656 - 664

IEEE Journals

 [AbstractPlus](#) | Full Text: [PDF](#) (1056 KB)


### Fault-tolerant serial-parallel multiplier

Chen, L.G.; Chen, T.H.;

[Computers and Digital Techniques, IEE Proceedings E](#)

Volume: 138 , Issue: 4

Publication Year: 1991 , Page(s): 276 - 280

IET Journals

 [AbstractPlus](#) | Full Text: [PDF](#) (360 KB)


### Improving Synthesis of Compressor Trees on FPGAs via Integer Linear Programming

Parandeh-Afshar, H.; Brisk, P.; Jenne, P.;

[Design, Automation and Test in Europe, 2008. DATE '08](#)Digital Object Identifier: [10.1109/DATE.2008.4484851](#)

Publication Year: 2008 , Page(s): 1256 - 1261

IEEE Conferences

 [AbstractPlus](#) | Full Text: [PDF](#) (352 KB)


### Design of a Reed-Solomon decoder using a digital signal processor (DSP)

Todoroki, T.; Miura, S.;

[Telecommunications Symposium, 1990. ITS '90 Symposium Record., SBT/IEEE International](#)Digital Object Identifier: [10.1109/ITS.1990.175644](#)

Publication Year: 1990 , Page(s): 443 - 445

IEEE Conferences

 [AbstractPlus](#) | Full Text: [PDF](#) (176 KB)


## A New Architecture for a SineWave Output DDS with High Spectral Purity

Kroupa, V.;

Direct Digital Frequency Synthesizers

Digital Object Identifier: 10.1109/9780470544396.ch48

Publication Year: 1998 , Page(s): 352 - 357

Copyright Year: 1999

IEEE BOOK CHAPTER

[Abstract](#) | [PDF](#)



## A direct digital frequency synthesizer using an IIR filter implemented with a DSP microprocessor

Lo Presti, L.; Cardamone, G.;

Acoustics, Speech, and Signal Processing, 1994. ICASSP-94., 1994 IEEE International Conference on

Volume: iii

Digital Object Identifier: 10.1109/ICASSP.1994.390055

Publication Year: 1994 , Page(s): III/201 - III/204 vol.3

IEEE Conferences



[AbstractPlus](#) | Full Text: [PDF](#) (268 KB)



[Quick & Abstract](#)



## An FPGA-specific algorithm for direct generation of multi-variate Gaussian random numbers

Thomas, David B.; Luk, Wayne;

Application-specific Systems Architectures and Processors (ASAP), 2010 21st IEEE International Conference on

Digital Object Identifier: 10.1109/ASAP.2010.5541005

Publication Year: 2010 , Page(s): 208 - 215

IEEE Conferences



[AbstractPlus](#) | Full Text: [PDF](#) (236 KB)



[Quick & Abstract](#)



## Novel approach for implementing convolutions with small tables

Skavantzos, A.;

Computers and Digital Techniques, IEE Proceedings E

Volume: 138 , Issue: 4

Publication Year: 1991 , Page(s): 255 - 259

IET Journals

 [AbstractPlus](#) | Full Text: [PDF](#) (308 KB)



### Fast Division on Fixed-Point DSP Processors Using Newton-Raphson Method

Nenadio, N.M.; Mladenovic, S.B.;

Computer as a Tool, 2005. EUROCON 2005. The International Conference on

Volume: 1

Digital Object Identifier: [10.1109/EURCON.2005.1630028](#)

Publication Year: 2005 , Page(s): 705 - 708

IEEE Conferences

 [AbstractPlus](#) | Full Text: [PDF](#) (1249 KB)



### Improved multiplier of CSD used in digital signal processing

Lei Chen; Xiao-Yan Tian; Xiao-Jun Zhao;

Machine Learning and Cybernetics, 2008 International Conference on

Volume: 5

Digital Object Identifier: [10.1109/ICMLC.2008.4620904](#)

Publication Year: 2008 , Page(s): 2905 - 2908

IEEE Conferences

 [AbstractPlus](#) | Full Text: [PDF](#) (277 KB)



### Novel input coding technique for high-precision LUT-based multiplication for DSP applications

Meher, P.K.;

VLSI System on Chip Conference (VLSI-SoC), 2010 18th IEEE/IFIP

Digital Object Identifier: [10.1109/VLSISOC.2010.5642660](#)

Publication Year: 2010 , Page(s): 201 - 206

IEEE Conferences

 [AbstractPlus](#) | Full Text: [PDF](#) (311 KB)



## New Approach to Look-Up-Table Design and Memory-Based Realization of FIR Digital Filter

Meher, P. K.;

Circuits and Systems I: Regular Papers, IEEE Transactions on

Volume: 57, Issue: 3

Digital Object Identifier: 10.1109/TCSI.2009.2026683

Publication Year: 2010, Page(s): 592 - 603

IEEE Journals



AbstractPlus | Full Text: PDF (1329 KB)



## Low power DCT implementation approach for VLSI DSP processors

Masupe, S.; Arslan, T.;

Circuits and Systems, 1999. ISCAS '99. Proceedings of the 1999 IEEE International Symposium on

Volume: 1

Digital Object Identifier: 10.1109/ISCAS.1999.777825

Publication Year: 1999, Page(s): 149 - 152 vol.1

IEEE Conferences



AbstractPlus | Full Text: PDF (284 KB)



## LUT Optimization for Memory-Based Computation

Meher, P.K.;

Circuits and Systems II: Express Briefs, IEEE Transactions on

Volume: 57, Issue: 4

Digital Object Identifier: 10.1109/TCSII.2010.2043467

Publication Year: 2010, Page(s): 285 - 289

IEEE Journals



AbstractPlus | Full Text: PDF (387 KB)



## Parametric architecture for implementing multimedia algorithms

Kavvadias, N.; Nikolaidis, S.;

Digital Signal Processing, 2002. DSP 2002. 2002 14th International Conference on

Volume: 2

Digital Object Identifier: [10.1109/ICDSP.2002.1028322](https://doi.org/10.1109/ICDSP.2002.1028322)

Publication Year: 2002 , Page(s): 1261 - 1264 vol.2

IEEE Conferences



[AbstractPlus](#) | Full Text: [PDF](#) (330 KB)



[Quick & Abstract](#)



## A flexible DSP block to enhance FPGA arithmetic performance

Parandeh-Afshar, Hadi; Cevrero, Alessandro; Athanasopoulos, Panagiotis; Brisk, Philip; Leblebici, Yusuf; Ienne, Paolo;

Field-Programmable Technology, 2009. FPT 2009. International Conference on

Digital Object Identifier: [10.1109/FPT.2009.5377638](https://doi.org/10.1109/FPT.2009.5377638)

Publication Year: 2009 , Page(s): 70 - 77

IEEE Conferences



[AbstractPlus](#) | Full Text: [PDF](#) (151 KB)



[Quick & Abstract](#)



## A hybrid number system processor with geometric and complex arithmetic capabilities

Lai, F.-S.; Wu, C.-F.E.;

Computers, IEEE Transactions on

Volume: 40 , Issue: 8

Digital Object Identifier: [10.1109/12.83639](https://doi.org/10.1109/12.83639)

Publication Year: 1991 , Page(s): 952 - 962

IEEE Journals



[AbstractPlus](#) | Full Text: [PDF](#) (844 KB)



[Quick & Abstract](#)



## Torque ripple optimization in switched reluctance motor using two-phase model and optimization search technique

Bhiwapurkar, N.; Mohan, N.;

Power Electronics, Electrical Drives, Automation and Motion, 2006. SPEEDAM 2006. International Symposium on

Digital Object Identifier: [10.1109/SPEEDAM.2006.1649795](https://doi.org/10.1109/SPEEDAM.2006.1649795)



Publication Year: 2006 , Page(s): 340 - 345

IEEE Conferences

 [AbstractPlus](#) | Full Text: [PDF](#) (287 KB)

 [Quick & Abstract](#)



A reconfigurable Digital Signal Processor using residue number system

Banerjee, S.; Sinha, A.;  
[Information Sciences Signal Processing and their Applications \(ISSPA\), 2010 10th International Conference on](#)  
Digital Object Identifier: [10.1109/ISSPA.2010.5605457](#)  
Publication Year: 2010 , Page(s): 405 - 408

IEEE Conferences

 [AbstractPlus](#) | Full Text: [PDF](#) (1706 KB)

 [Quick & Abstract](#)

- 1
- 2

Refine/Expand Results

Search within results:  

☐ Only show full text results included in my subscription

Content Type

- ☐ Conferences (24)
- ☐ Journals (9)
- ☐ Books (1)

Publication Year

☐ Single Year ☒ Range

1987 2010

From:

To:

Author

Search for Author



- 
- Meher, P.K. (2)
- 
- Luk, Wayne (2)
- 
- Thomas, David B. (2)
- 
- Morris, G.W. (1)
- 
- Miura, S. (1)
- 
- Cheung, P.Y.K. (1)
- 
- Bose, B.K. (1)
- 
- Cardarilli, G.C. (1)
- 
- Lojacono, R. (1)
- 
- Banerjee, S. (1)
- 
- Piazza, F. (1)
- 
- Mohan, N. (1)
- 
- Lo Presti, L. (1)
- 
- Sinha, A. (1)
- 
- Orlandi, G. (1)
- 
- Stearns, C.C. (1)
- 
- Jain, V.K. (1)
- 
- Skavantzios, A. (1)
- 
- Delgado-Frias, J.G. (1)
- 
- Di Claudio, E.D. (1)
- 
- Soderstrand, M.A. (1)
- 
- Bayoumi, M. (1)
- 
- Wu, C.-F.E. (1)
- 
- Todoroki, T. (1)
- 
- Chen, L.G. (1)

Affiliation

Search for Affiliation

- ☐ IBM Thomas J. Watson Res. Center, Yorktown Heights, NY (1)
- ☐ Dept. of Electr. Eng., Nat. Taiwan Univ., Taipei, Taiwan (1)
- ☐ Dipartimento di Elettronica, Politecnico di Torino (1)
- ☐ Dept. of Electr. Eng., Assiut Univ. (1)
- ☐ NEC Corp., Tokyo (1)
- ☐ Dept. of Electron. Eng., Rome Univ. (1)
- ☐ Dept. of Electr. Eng., Univ. of South Florida, Tampa, FL (1)
- ☐ Dept. of Electr. & Comput. Eng., Louisiana State Univ., Baton Rouge, LA, USA (1)
- ☐ Sch. of Electr. & Comput. Eng., Oklahoma State Univ., Stillwater, OK, USA (1)
- ☐ INFOCOM Dept., Rome Univ. (1)
- ☐ Mihailo Pupin Inst., Belgrade (1)
- ☐ LSI Logic Corp. (1)
- ☐ Microelectron. Lab., CDTA, Algiers (1)
- ☐ COPPE, Univ. Fed. do Rio de Janeiro (1)
- ☐ Imperial College London (1)
- ☐ Dept. of ECE, Minnesota Univ., Minneapolis, MN (1)
- ☐ Dept. of Electron. & Electr. Eng., Imperial Coll., London, UK (1)
- ☐ Dept. of Phys., Aristotle Univ., Thessaloniki, Greece (1)
- ☐ University of Southwestern Louisiana, Lafayette, LA (1)
- ☐ Dep. de Eng. Eletrica, Univ. Fed. de Mato Grosso Sul, Brazil (1)
- ☐ Pacific Northwest Nat. Lab., Richland (1)
- ☐ Coll. of Electron. & Inf. Eng., Hebei Univ., Baoding (1)
- ☐ Sch. of Electr. & Comput. Eng., Univ. of Tehran, Tehran (1)
- ☐ Dept. of Electron. & Electr. Eng., Edinburgh Univ. (1)
- ☐ Digital Media Lab., Beihang Univ., Beijing, China (1)

Publication Title

Search for Publication Title

- 
- Computers, IEEE Transactions on (2)
  - Computers and Digital Techniques, IEE Proceedings E (2)
  - Circuits and Systems I: Regular Papers, IEEE Transactions on (2)
  - Field-Programmable Technology, 2009. FPT 2009. International Conference on (2)
  - Components, Hybrids, and Manufacturing Technology, IEEE Transactions on (1)
  - Telecommunications Symposium, 1990. ITS '90 Symposium Record., SBT/IEEE International (1)
  - Signal Processing Magazine, IEEE (1)
  - Acoustics, Speech, and Signal Processing, 1994. ICASSP-94., 1994 IEEE International Conference on (1)
  - Microelectronics, 1997. Proceedings., 1997 21st International Conference on (1)
  - Circuits and Systems, 1997. ISCAS '97., Proceedings of 1997 IEEE International Symposium on (1)
  - FPGAs for Custom Computing Machines, 1998. Proceedings. IEEE Symposium on (1)
  - Signals, Systems and Computers, 1988. Twenty-Second Asilomar Conference on (1)
  - Circuits and Systems, 1999. ISCAS '99. Proceedings of the 1999 IEEE International Symposium on (1)
  - Electronics, Circuits and Systems, 1999. Proceedings of ICECS '99. The 6th IEEE International Conference on (1)
  - Digital Signal Processing, 2002. DSP 2002. 2002 14th International Conference on (1)
  - Acoustics, Speech, and Signal Processing, IEEE International Conference on ICASSP '87. (1)
  - Circuits and Systems, 2003. ISCAS '03. Proceedings of the 2003 International Symposium on (1)
  - Circuits and Systems II: Express Briefs, IEEE Transactions on (1)
  - Industrial Electronics Society, 2004. IECON 2004. 30th Annual Conference of IEEE (1)
  - Field Programmable Logic and Applications, 2005. International Conference on (1)
  - Computer as a Tool, 2005. EUROCON 2005.The International Conference on (1)
  - Power Electronics, Electrical Drives, Automation and Motion, 2006. SPEEDAM 2006. International Symposium on (1)
  - Design, Automation and Test in Europe, 2008. DATE '08 (1)
  - Machine Learning and Cybernetics, 2008 International Conference on (1)

☐ Information Science and Engineering (ICISE), 2009 1st International Conference on (1)

**Publisher**

- ☐ IEEE (32)
- ☐ IET (2)

**Subject**

















- ☐ Components, Circuits, Devices & Systems (23)
- ☐ Computing & Processing (Hardware/Software) (15)
- ☐ Communication, Networking & Broadcasting (10)
- ☐ Signal Processing & Analysis (10)
- ☐ Engineered Materials, Dielectrics & Plasmas (8)
- ☐ Power, Energy, & Industry Applications (5)
- ☐ Fields, Waves & Electromagnetics (3)
- ☐ Robotics & Control Systems (2)
- ☐ Bioengineering (2)
- ☐ Aerospace (1)
- ☐ Geoscience (1)
- ☐ Transportation (1)

**Conference Country**

- ☐ USA (2)
- ☐ Hong Kong (1)
- ☐ Brazil (1)
- ☐ Australia (1)
- ☐ Yugoslavia (1)
-

 Cyprus (1)

Conference Location

-  Sydney, NSW (2)
-  Orlando, FL (1)
-  Napa Valley, CA (1)
-  Rio de Janeiro (1)
-  Munich (1)
-  Adelaide, SA (1)
-  Nis (1)
-  Shanghai (1)
-  Madrid (1)
-  Pafos (1)
-  Nanjing (1)
-  Belgrade (1)
-  Kuala Lumpur (1)
-  Taormina (1)
-  Kunming (1)
-  Rennes, France (1)

Brought to you by

United States Patent and Trademark Office

- Your institute subscribes to:
- IEEE/IET Electronic Library (IEL), IEEE Draft Standards Online Subscription, IBM Journal of Research and Development
- What can I access?

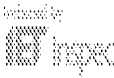
[Terms of Use](#)

SEARCHES FOR THIS SESSION

- [multiplication with DSP and table \(34\)](#)
- [More Search History](#)

Additional Results

[Application Notes \(beta\)](#)



[Help](#) | [Contact Us](#) | [Privacy & Security](#) | [Site Map](#) | [IEEE.org](#) | [Nondiscrimination Policy](#) | [Terms of Use](#)

© Copyright 2011 IEEE – All Rights Reserved

[Back to Top](#)

[Skip to Main Content](#)

[IEEE.org](#) | [IEEE Xplore Digital Library](#) | [IEEE Standards Association](#) | [Spectrum Online](#) | [More IEEE Sites](#)



Search Term(s)


[Advanced Search](#) | [Preferences](#) | [Search Tips](#)






**[Browse](#)**

- [Journals & Magazines](#)
- [Conference Proceedings](#)
- [Standards](#)
- [Books](#)
- [Educational Courses](#)
- [Technology Surveys](#)
  
- [My Settings](#)
  - [Alerts](#)
  - [Purchase History](#)
  - [Saved Searches](#)
  - [Sign In](#)
  - [What can I access?](#)
- [Cart](#)

[Terms of Use](#) | [Feedback](#) |  [Help](#)

**Search Results**

You searched for: **multiplication with DSP and table , lut** 

-  [Set Defaults](#)
-  [Download](#)
-  [Download](#)
-  [Download](#)
-  [Print](#)

Results per Page:

Showing 1 - 7 of 7 results

Sort By:

[Select All on Page](#) | [Deselect All](#)





## LUT Optimization for Memory-Based Computation

Meher, P.K.;

Circuits and Systems II: Express Briefs, IEEE Transactions on

Volume: 57, Issue: 4

Digital Object Identifier: [10.1109/TCSII.2010.2043467](https://doi.org/10.1109/TCSII.2010.2043467)

Publication Year: 2010, Page(s): 285 - 289

IEEE Journals



[AbstractPlus](#) | Full Text: [PDF](#) (387 KB)



## Novel input coding technique for high-precision LUT-based multiplication for DSP applications

Meher, P.K.;

VLSI System on Chip Conference (VLSI-SoC), 2010 18th IEEE/IFIP

Digital Object Identifier: [10.1109/VLSISOC.2010.5642660](https://doi.org/10.1109/VLSISOC.2010.5642660)

Publication Year: 2010, Page(s): 201 - 206

IEEE Conferences



[AbstractPlus](#) | Full Text: [PDF](#) (311 KB)



## New Approach to Look-Up-Table Design and Memory-Based Realization of FIR Digital Filter

Meher, P. K.;

Circuits and Systems I: Regular Papers, IEEE Transactions on

Volume: 57, Issue: 3

Digital Object Identifier: [10.1109/TCSI.2009.2026683](https://doi.org/10.1109/TCSI.2009.2026683)

Publication Year: 2010, Page(s): 592 - 603

IEEE Journals



[AbstractPlus](#) | Full Text: [PDF](#) (1329 KB)



## Parametric architecture for implementing multimedia algorithms

Kavvadias, N.; Nikolaidis, S.;

Digital Signal Processing, 2002. DSP 2002. 2002 14th International Conference on

Volume: 2

Digital Object Identifier: [10.1109/ICDSP.2002.1028322](#)

Publication Year: 2002 , Page(s): 1261 - 1264 vol.2

IEEE Conferences

 [AbstractPlus](#) | Full Text: [PDF](#) (330 KB)



## CSD multipliers for FPGA DSP applications

Soderstrand, M.A.;

[Circuits and Systems, 2003. ISCAS '03. Proceedings of the 2003 International Symposium on](#)

Volume: 5

Digital Object Identifier: [10.1109/ISCAS.2003.1206319](#)

Publication Year: 2003 , Page(s): V-469 - V-472 vol.5

IEEE Conferences

 [AbstractPlus](#) | Full Text: [PDF](#) (329 KB)



## Using DSP blocks for ROM replacement: a novel synthesis flow

Morris, G.W.; Constantimides, G.A.; Cheung, P.Y.K.;

[Field Programmable Logic and Applications, 2005. International Conference on](#)

Digital Object Identifier: [10.1109/FPL.2005.1515702](#)

Publication Year: 2005 , Page(s): 77 - 82

IEEE Conferences

 [AbstractPlus](#) | Full Text: [PDF](#) (205 KB)



## Improving Synthesis of Compressor Trees on FPGAs via Integer Linear Programming

Parandeh-Afshar, H.; Brisk, P.; lenne, P.;

[Design, Automation and Test in Europe, 2008. DATE '08](#)

Digital Object Identifier: [10.1109/DATE.2008.4484851](#)

Publication Year: 2008 , Page(s): 1256 - 1261

IEEE Conferences

 [AbstractPlus](#) | [Full Text: PDF](#) (352 KB)



Refine/Expand Results

Search within results:

☐ Only show full text results included in my subscription

Content Type

- ☐ Conferences (5)
- ☐ Journals (2)

Publication Year

☐ Single Year ☒ Range

2002 2010

From:

To:

Author

- ☐ Meher, P.K. (2)
- ☐ Morris, G.W. (1)
- ☐ Cheung, P.Y.K. (1)
- ☐ Soderstrand, M.A. (1)
- ☐ Nikolaidis, S. (1)
- ☐ Ienne, P. (1)
- ☐ Kavvadias, N. (1)
- ☐ Brisk, P. (1)
- ☐ Constantimdes, G.A. (1)
- ☐ Parandeh-Afshar, H. (1)
- ☐ Meher, P. K. (1)

Affiliation

-

- ☐ Sch. of Electr. & Comput. Eng., Oklahoma State Univ., Stillwater, OK, USA (1)
- 
- ☐ Dept. of Electron. & Electr. Eng., Imperial Coll., London, UK (1)
- 
- ☐ Dept. of Phys., Aristotle Univ., Thessaloniki, Greece (1)
- 
- ☐ Sch. of Electr. & Comput. Eng., Univ. of Tehran, Tehran (1)
- 
- ☐ Dept. of Commun. Syst., Inst. for Infocomm Res., Singapore, Singapore (1)
- 
- ☐ Dept. of Embedded Syst., Inst. for Infocomm Res., Singapore, Singapore (1)

Publication Title

- 
- ☐ Digital Signal Processing, 2002. DSP 2002. 2002 14th International Conference on (1)
- 
- ☐ Circuits and Systems, 2003. ISCAS '03. Proceedings of the 2003 International Symposium on (1)
- 
- ☐ Circuits and Systems I: Regular Papers, IEEE Transactions on (1)
- 
- ☐ Circuits and Systems II: Express Briefs, IEEE Transactions on (1)
- 
- ☐ Field Programmable Logic and Applications, 2005. International Conference on (1)
- 
- ☐ Design, Automation and Test in Europe, 2008. DATE '08 (1)
- 
- ☐ VLSI System on Chip Conference (VLSI-SoC), 2010 18th IEEE/IFIP (1)

Subject

- 
- ☐ Components, Circuits, Devices & Systems (7)
- 
- ☐ Communication, Networking & Broadcasting (3)
- 
- ☐ Computing & Processing (Hardware/Software) (3)
- 
- ☐ Engineered Materials, Dielectrics & Plasmas (2)
-

- ☐ Signal Processing & Analysis (2)
- ☐ Fields, Waves & Electromagnetics (1)
- ☐ Power, Energy, & Industry Applications (1)

Conference Location

- ☐ Munich (1)
- ☐ Madrid (1)

Brought to you by

United States Patent and Trademark Office

- Your institute subscribes to:
- IEEE/ET Electronic Library (IEL), IEEE Draft Standards Online Subscription, IBM Journal of Research and Development
- [What can I access?](#)

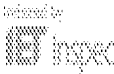
[Terms of Use](#)

SEARCHES FOR THIS SESSION

- [multiplication with DSP and table, lut \(7\)](#)
- [multiplication with DSP and table \(34\)](#)
- [More Search History](#)

Additional Results

[Application Notes \(beta\)](#)



[Help](#) | [Contact Us](#) | [Privacy & Security](#) | [Site Map](#) | [IEEE.org](#) | [Nondiscrimination Policy](#) | [Terms of Use](#)

© Copyright 2011 IEEE – All Rights Reserved

[Back to Top](#)